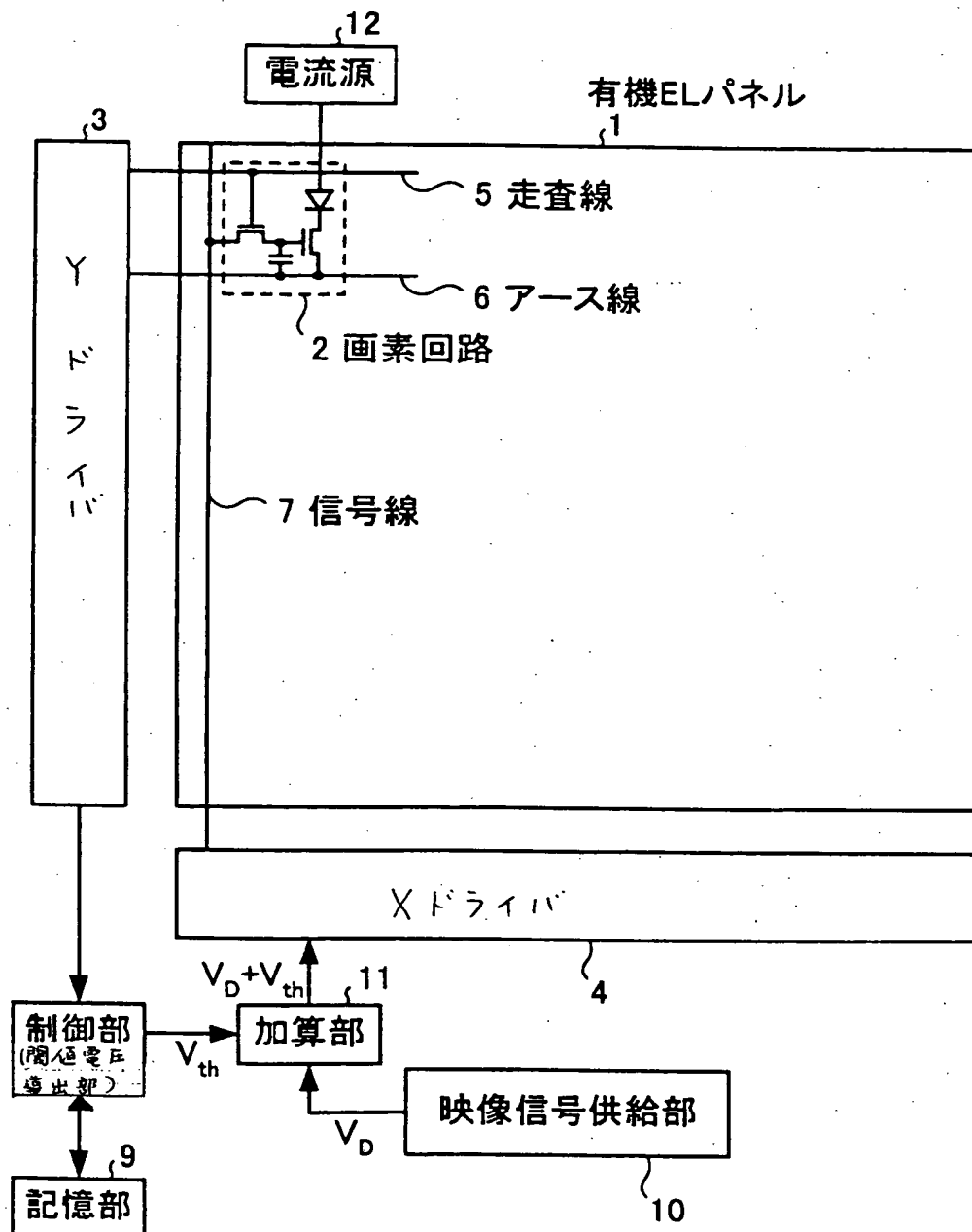


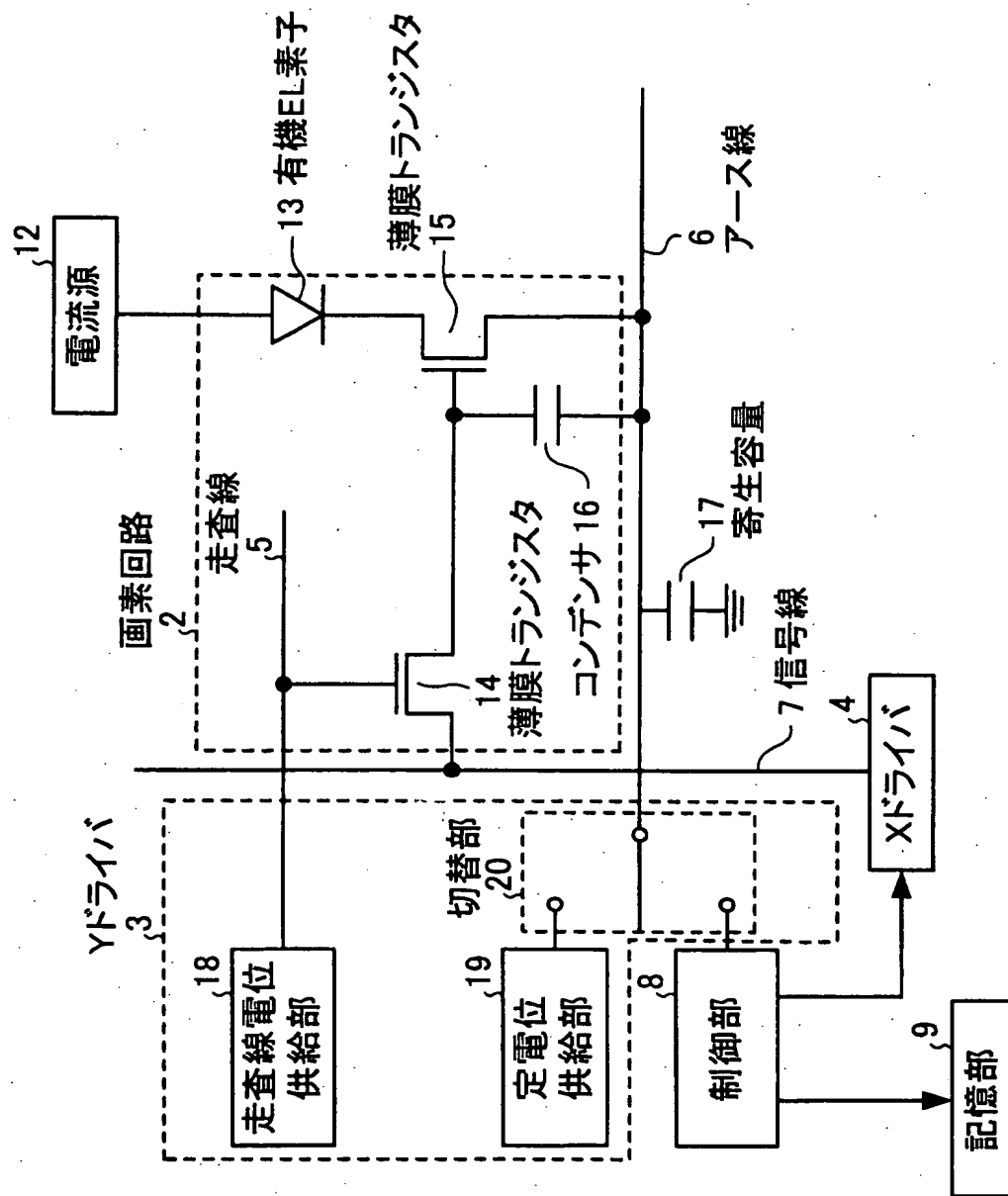
【書類名】

図面

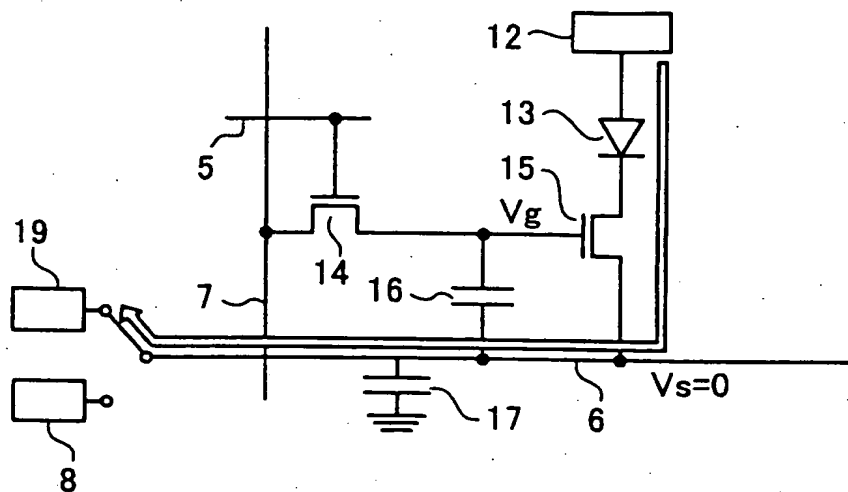
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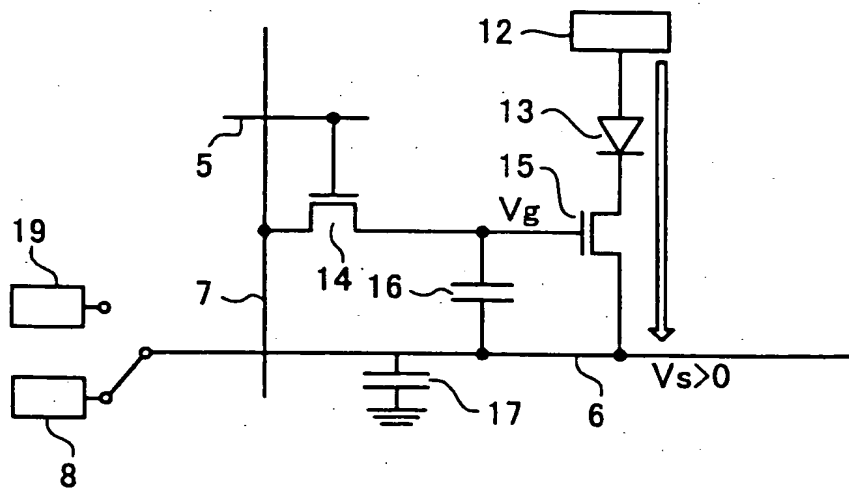
【図2】



【図 3 A】



【図 3 B】



【図 3 C】

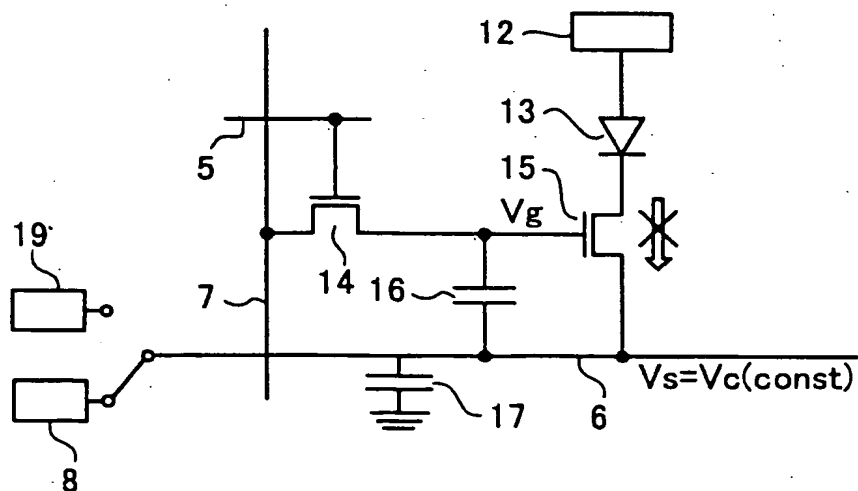
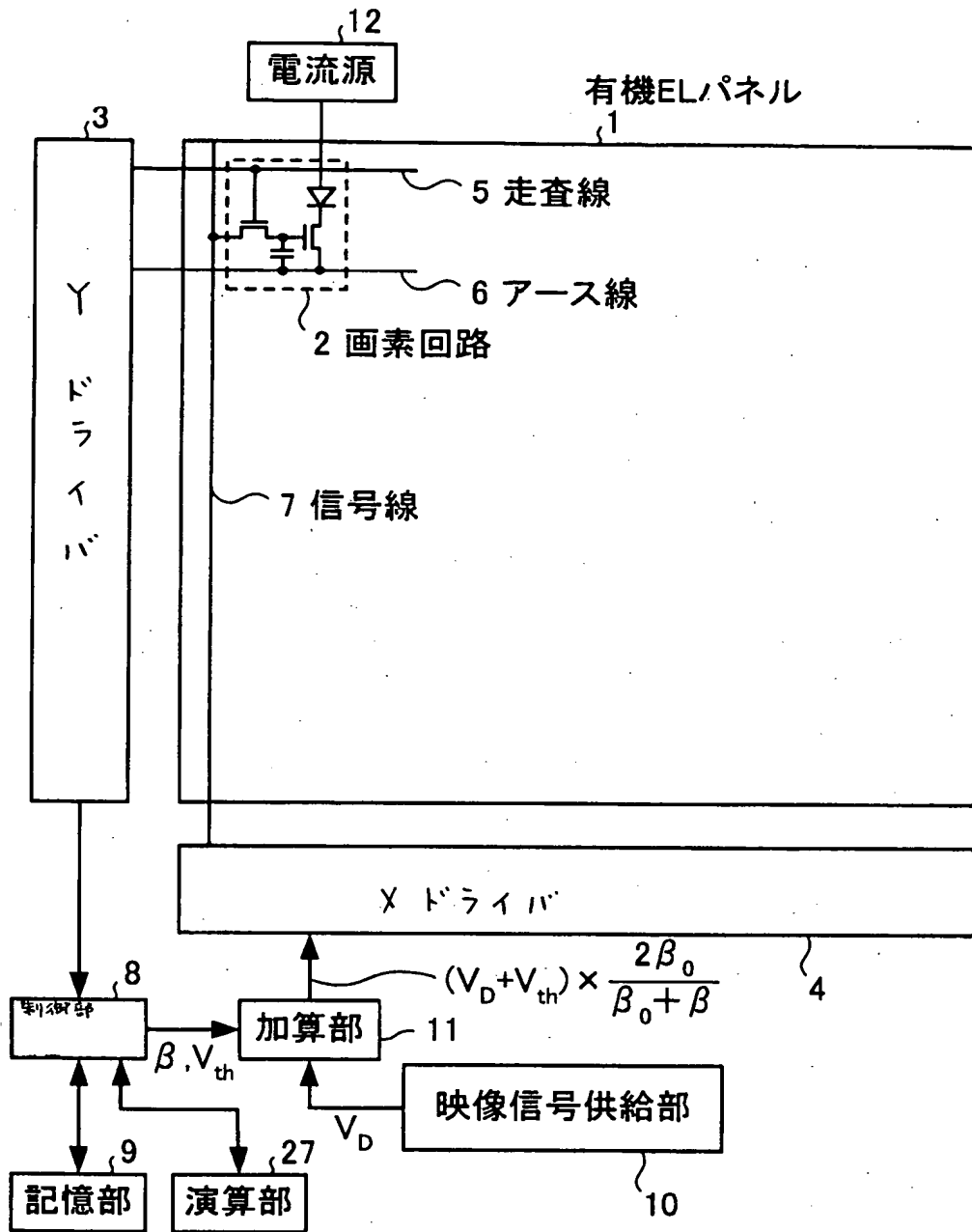
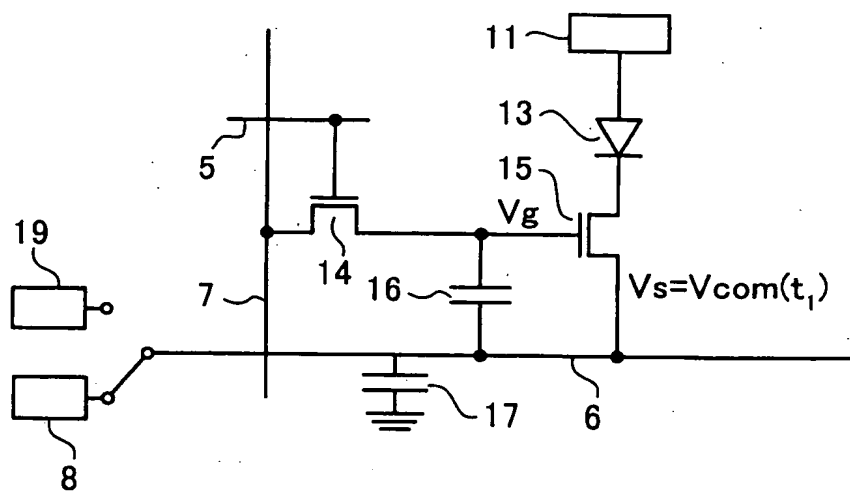


Figure 1 is a block diagram of a Y-axis driver circuit. The circuit includes a Y-axis driver unit 3n-1, a Y-axis driver unit 3n, and a Y-axis driver unit 3n+1. The Y-axis driver unit 3n-1 consists of a selection unit 21, an AD converter 23, a selection unit 24, and a latch unit 25. The Y-axis driver unit 3n consists of a selection unit 22a, an AD converter 23, a selection unit 24, and a latch unit 25. The Y-axis driver unit 3n+1 consists of a selection unit 22b, an AD converter 23, a selection unit 24, and a latch unit 25. The circuit is connected to a ground line 17 and a power supply line 18. The circuit is also connected to a Y-axis driver unit 3n-1 and a Y-axis driver unit 3n+1.

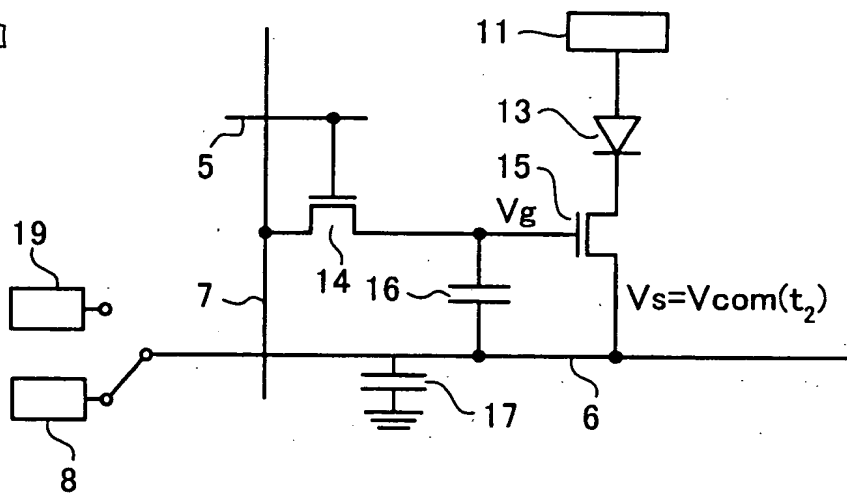
【図 5】



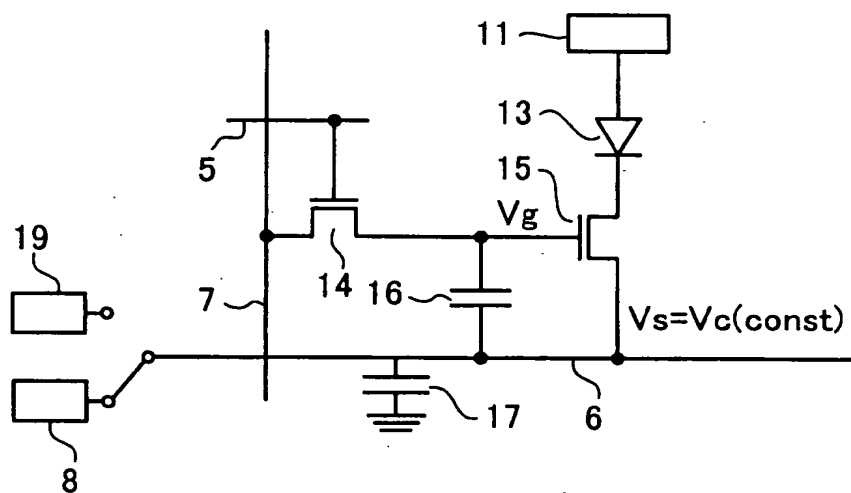
【図 6 A】



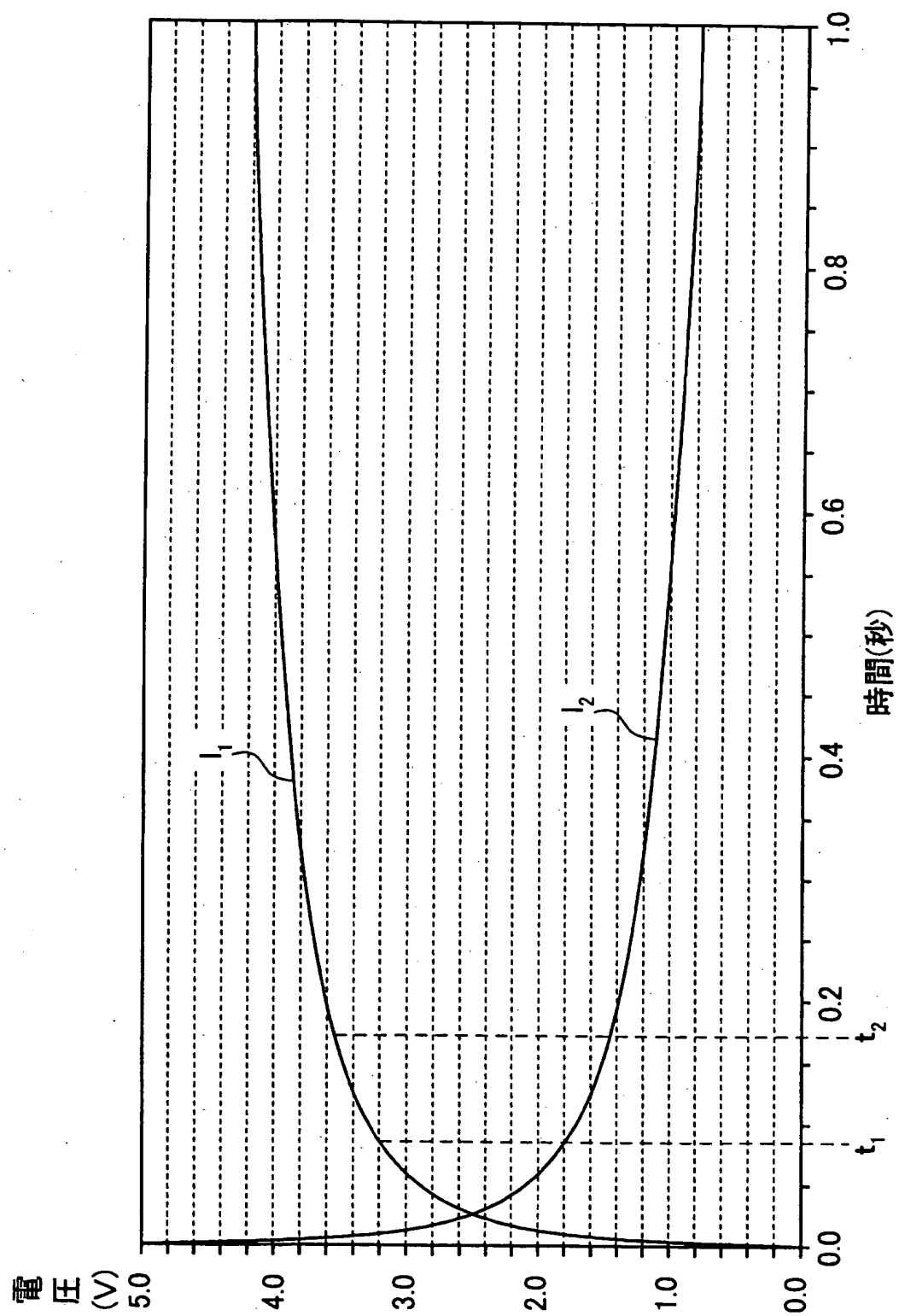
【図 6 B】



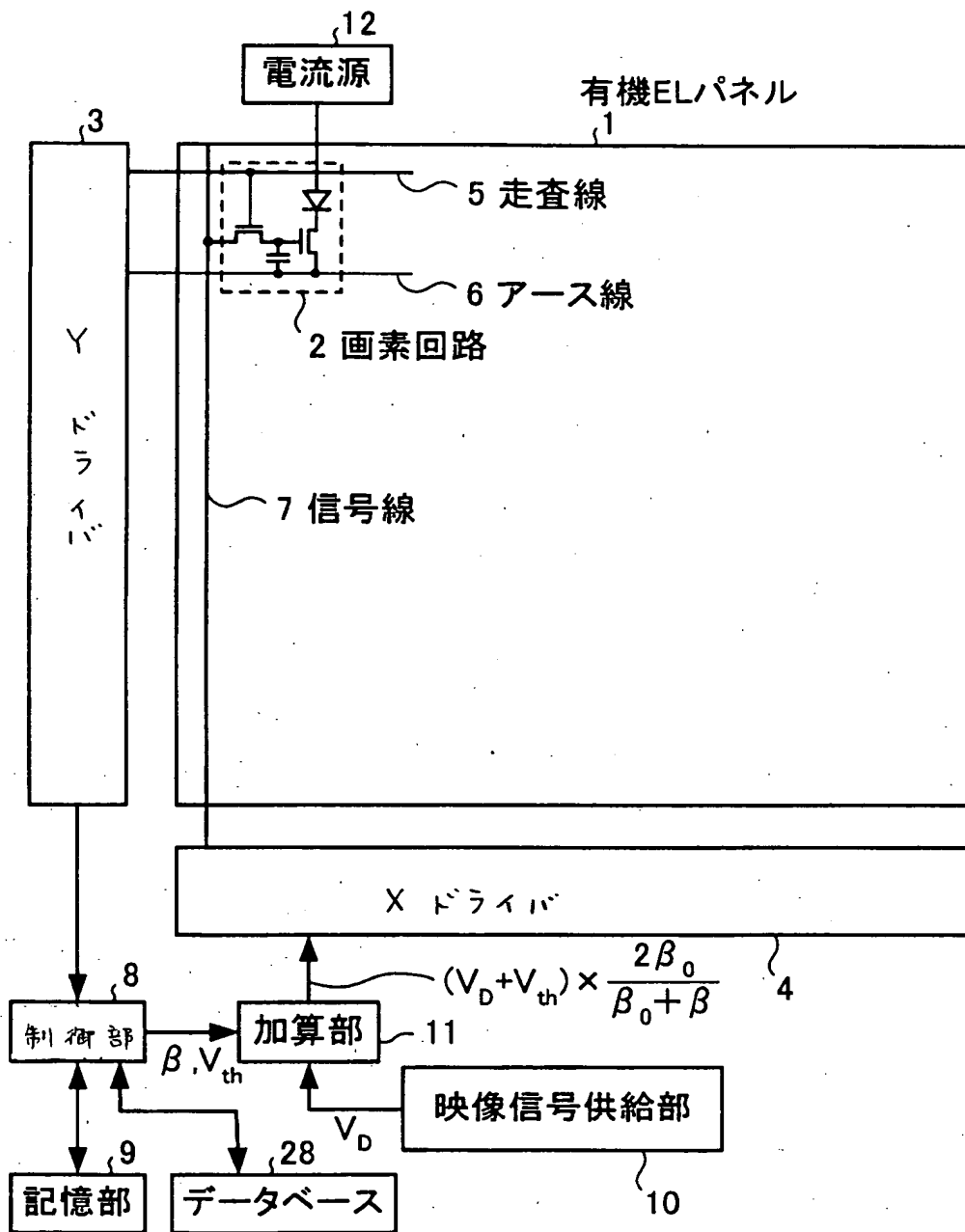
【図 6 C】



【図 7】



【図 8】





【図 9】

